

FDPs / SDPs Organized

2018-19

S.No	Name of the FDP/ SDP	Name of the Coordinator(s)	Resource Person(s)	Date(s)			No. of Faculty Attended External/Internal
				From	To	No. of Days	
1.	5G and GNSS Positioning with Hands-on Sessions using MATLAB	B. Hari Kumar - convener Dr. P. Vijai Bhaskar - Co-convener. Dr. V. Satya Srinivas -Coordinator	Dr. M.V.S.N Prasad (Former Chief-Scientist, NPL, New Delhi, Dr. Prashant Kumar Misra, Scientist - F, RCI, DRDO, Dr. A. Supraja Reddy, and others	17-Jun-19	22-Jun-19	6	20 External
2.	Digital Systems Design	Ch. Sandeep	Prof. K. Somasekhara Rao	15-Nov-18	17-Nov-18	3	15 Internal
3.	Electromagnetic & its with hands on session using MATLAB	Dr. R.S.Raju	Dr. V M Pandari Pande, Dr. P V D Somasekhara Rao, Dr. V. Sarala and Dr. N V S Sarma	26-Nov-18	1-Dec-18	6	26 Internal 19 External
4.	FDP on Analog Communications Lab	Dr.P.Vijai Bhasker	Dr.P.Vijai Bhasker And others	08-Dec-18		1	10 Internal
5.	FDP on Simulation lab-II Laboratory	Dr. .P. Vijai Bhaskar	Dr. .P. Vijai Bhaskar and others	06-Dec-18	08-Dec-18	3	12 Internal
6.	FDP on Digital Signal Processing Laboratory experiments	Dr. C.V Narasimhulu	Dr. C.V Narasimhulu & Ms. L. Kavya	05-Dec-18	06-Dec-18	2	12 Internal
7.	FDP on VLSI Laboratory	Prof OVPR Siva Kumar	Prof OVPR Siva Kumar & B. Jugal Kishore	03-Dec-18	04-Dec-18	2	7 Internal
8.	FDP on Xilinx vivado tool	Prof OVPR Siva Kumar	Prof OVPR Siva Kumar & Ms. S.Yagnasree	03-Dec-18	04-Dec-18	2	7 Internal
9.	FDP on Digital Signal Processing Laboratory experiments	Dr. C.V Narasimhulu	Dr. C.V Narasimhulu	05-Dec-18	06-Dec-18	2	7 Internal
10.	FDP on Digital System Design Course	Prof. K.Somasekhara Rao	Dr. S. Udaya Kumar	15-Nov-18	17-Nov-18	3	15 Internal

11.	FDP on Simulation lab-II Laboratory experiments	Prof.P.Vijai Bhaskar	Prof.P.Vijai Bhaskar	06-Dec-18	08-Dec-18	2	7 Internal
12.	5 Day In house FDP on Embedded Systems Lab	Ch.Sandeep	Prof. K. Somasekhara Rao	15-Apr-19	20-Apr-19	6	15 Internal
13.	5 Day in house FDP on Digital Design Lab	D.Sony	Prof. K. Somasekhara Rao	24-Apr-19	29-Apr-19	6	13 Internal
14.	Guidelines for Application Specific Integrated Circuit (ASIC) Designs	Prof OVPR Siva Kumar	Prof OVPR Siva Kumar	18-May-19		1	10 Internal
15.	Three-day FDP in MPMC Lab	Ms. G. Sree Lakshmi and Ms. M.Laxmi	Ms. G. Sree Lakshmi and Ms. M.Laxmi	17-Jun-19	19-Jun-19	2	10 Internal
16.	Three day FDP in MWE & DC Lab	Dr.R.S Raju and Dr.B.L Prakash	Dr.R.S Raju and Dr.B.L Prakash	19-Jun-19	21-Jun-19	3	10 Internal